

REGISTRATION FORM

Two Day Workshop On

Applications of Field Programmable Analog Array (FPAA) in VLSI 8- 9 April 2019

Name: _____

Designation: _____

Organization: _____

Mailing Address: _____

Telephone: _____

Fax: _____

Email: _____

Registration fee Details

Amount: _____

Mode of Payment: _____

Date: _____ Signature of Applicant

I hereby forward and recommend the above applicant for attending the workshop on “*Applications of field programmable analog array (FPAA) in VLSI*”, 8-9 April 2019.

Signature of
Head of Institute with seal.

Patron

Prof. Goutam Sutradhar
Director, NIT Manipur

Chairman

L.Surajkumar
HOD, ECE

Coordinators

Mr. Manoj Kumar
Mr. Richard

Address for Communication

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Two Day Workshop On

Applications of Field Programmable Analog Array (FPAA) in VLSI

8- 9 April 2019



Sponsored by:

Technical Education Quality
Improvement Programme – III

Organized by

Department of Electronics &
Communication Engineering
National Institute of Technology Manipur,
Imphal-795004 India

ABOUT THE INSTITUTE

National Institute of Technology Manipur, a centrally funded institution is set up to impart quality technical education at various levels of higher learning. It is one of the ten new NITs established and developed as Institute of National Importance by an act of Parliament. NIT Manipur started its first session with the three branches of Engineering-Electrical & Electronics Engineering, Electronics & Communication Engineering and Computer Science Engineering. The functioning of the institute was started at its temporary Campus at Takyelpat, Imphal under the mentorship of NIT, Agartala. The Institute has acquired 138.2 hectares of land in the lush green areas of Langol, Imphal and started its functioning at its permanent campus since 2014. This Institute now has five branches of Engineering viz CSE, EEE, ECE, Civil, Mechanical and Basic Sciences and Humanities Department and open courses on B. Tech., M. Tech., M. Sc. and Ph. D.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering came into existence in the year 2010. Presently the department offers B. Tech, M .Tech. and PhD program.

ABOUT THE IMPHAL CITY

Imphal is the capital of Manipur, a state in the north eastern India.. It is one of the loveliest spots in the different cultures, sports and good climate. It is the center of all cultural, commercial and political activities of the state. On the Imphal River, Kangla Fort was once the seat of local rulers and now houses relics. On the south side of the centuries old pologround, Manipur State Museum has displays including tribal artifacts and a large royal boat. Next to the former Royal Palace, Shri Shri Govindaji Temple has twin domes, a large hall and shrines to Hindu gods

ABOUT THE WORKSHOP

OBJECTIVE

The main aim and motivation of organizing this workshop is uses of Filed Programmable Analog Array (FPAA) in VLSI. At the end of workshop the participants are able to design, simulate analog systems/circuits and test the performance in few minutes.

COURSE CONTENT

1. Introduction to FPAA Anadigm Designer 2 - EDA Tool
2. EDA Tool –Simulation, FPAA Configuration
3. IO Pin Configuration ,Static Configuration
4. Audio Signal Processing in FPAA (Design and Implementation)

TARGET PARTICIPANTS

- Faculty members, UG/PG Engineering students and research scholars from all branches of engineering are welcome.

- Payment should be made only after the initial confirmation
- The total intake: 30
- Limited accommodation may be arrange for outstation participants on payment basis and first come first serve.

REGISTRATION DETAILS

Reg. Fee: Rs. 500/-

Dead Lines:

- Receipt of Application: 04.04.19
- Confirmation: 05.04.19
- Registration Fee payment: 08.04.19
- On spot registration may be avail for outstation participants.
- No TA/DA will be paid for participants.

Resource Person

Mr. R. RAJESH is a Embedded software Engineer at Oppila Microsystems Pvt, Ltd, Bangalore. He has done. B.Tech(ECE). He has 4 years of Experience in software development, and contributing various projects in DRDO, Bangalore and IGCAR, Kalpakkam.Working in Anadigm's Development and technical support more than 2 years and have given lectures in various universities about FPAA in India.