

Department of Electronics & Communication Engineering राष्ट्रीय प्रौद्योगिकी संस्थान,मणिपुर

NATIONAL INSTITUTE OF TECHNOLOGY MANIPUR

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7/9/2019

The following candidates are shortlisted for appearing in the entrance test for admission to Ph.D. (July 2019) in the department of Electronics and Communication Engineering. The detailed schedule of the written test and the interview are mentioned below.

Date of written test: 24th July 2019.

Venue: VLSI Lab, Langol

Time: 10 a.m. to 12 p.m.

Time of interview: 1:30 p.m.

Declaration of result: 4 p.m.

S1.	Name of the Candidate	Category	Gate/Non Gate
No			
1	Bharat Meitei Huirem	Full Time	Non Gate
2	Rosy Kimneithem haokip	Part Time	Gate
3	Manish Kumar Srivatava	Part Time	Gate
4	Patrick Yumnam	Full Time	Non Gate
5	Reena G	Full Time	Non Gate
6	Maddela Anil Kumar	Part Time	Non Gate
7	Thiyam Romila Devi	Full Time	Non Gate
8	Ningthoukhongjam Kunan	Full Time	Non Gate
9	Langpoklakpam David Singh	Part Time	Gate
10	Thingujam Churchil Singh	Part Time	Non Gate
11	Yumnam Shantikumar Singh	Part Time	Non Gate

(Dr. Manoj Kumar)

Asst. Prof., ECE

NIT Manipur