



Department of Electronics and communication Engineering
राष्ट्रीय प्रौद्योगिकी संस्थान, मणिपुर

NATIONAL INSTITUTE OF TECHNOLOGY, MANIPUR

Langol, Imphal. Ph. (0385) 2445812 / email:- nitmn@nitmanipur.ac.in

An Autonomous Institute under MHRD, Govt. of India.

Thesis/ Project Name of Master of Technology in Electronics and Communication Engineering
Department

2023 Batch			
Sl No.	Enrolment Number	Student Name	Thesis/Project Name
1.	21205002	PHEIROIJAM ROBIN LUWANG	DESIGN AND IMPLEMENTATION OF QUADCOPTER USING PID CONTROL ALGORITHM
2.	21205003	TONGBRAM BIDYASAGAR SINGH	IMPLEMENTATION OF DIFFERENT ORDER (FIRST, SECOND & THIRD) MOS ONLY LOW PASS FILTER
3.	21205004	TONGBRAM ARPAN MEITEI	DESIGN OF FULLY ON-CHIP FLASH ADC BASED DIGITAL LOW DROPOUT VOLTAGE REGULATORS IN GPDK 45NM CMOS TECHNOLOGY
4.	21205005	SHOUGAIJAM CHINGLEMBA SINGH	DESIGN AND IMPLEMENTATION OF SECURED WIRELESS COMMUNICATION BASED FIRE DETECTION SYSTEM
5.	21205006	NAOREM BHABINI DEVI	FPGA IMPLEMENTATION OF FOUR-LIFT CONTROL SYSTEM WITH SMOKE DETECTION
6.	21205007	MOIRANGTHEM RUSHDIE DEVI	PERFORMANCE ANALYSIS IN NOMA
7.	21205009	RUSKIN POTSANGBAM	DESIGN OF LOW POWER VOLTAGE REGULATOR FOR RFID APPLICATION USING GPDK 45NM TECHNOLOGY
8.	21205010	MOIRANGTHEM RAJU SINGH	DESIGN AND INVESTIGATION ON COMPACT FREQUENCY SELECTIVE SURFACE FOR WIRELESS COMMUNICATION



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2022 Batch

Sl No.	Enrolment Number	Student Name	Thesis/Project Name
1.	20205002	KHONGBANTABAM MAMOTA DEVI	STUDY AND DESIGN OF FRACTIONAL ORDER SYSTEM EMPLOYING ACTIVE BLOCKS
2.	20205003	RADHARANI YUMLEMBAM	STUDY AND DESIGN OF MEMCAPACITOR USING FTFNTA
3.	20205004	SHAMJETSHABAM NANDESHOWRI DEVI	SYNTHESIS AND CHARACTERIZATION OF TIO ₂ THIN FILM ASSISTED WITH AG NANOPARTICLE FOR THE APPLICATION OF BROAD-LIGHT DETECTOR
4.	20205005	GGERIN KONSAM	A STUDY ON HARDWARE ACCELERATORS FOR MACHINE LEARNING ALGORITHMS
5.	20205006	MAIBAM DONEGUE SINGH	AUTOMATED NUTRIENT SOLUTION DOSING IN HYDROPONICS SYSTEM
6.	20205007	THINGOM DEVACHANDRA SINGH	DESIGN DEVELOPMENT OF REAL TIME APPLICATIONS USING BLUTOOH AND FPGA
7.	20205008	KONGKHAM SUJALATA	DESIGN OF TIO ₂ NW EMBEDDED WITH SILVER NANOPARTICLES BY GLAD TECHNIQUE FOR EFFICIENT PHOTODETECTECTOR APPLICATION
8.	20205009	KELVEEN RAJ OINAM	DESIGN AND IMPLEMENTATION OF IOT BASED FACIAL RECOGNITION SYSTEM IN VEHICLES USING RASPBERRY PI
9.	20205010	SANASAM PREMANANDA SINGH	RICE PLANT DISEASE DETECTION AND CLASSIFICATION WITH CONVOLUTIONAL NEURAL NETWORKS
10.	20205011	NGANGOM SUNIL CHANDRA SINGH	DEVELOPMENT OF POLARIZATION INSENSITIVE AND ANGULARLY STABLE MUTI BAND-STOP FRQUENCY SELECTIVE SURFACE



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2021 Batch			
Sl No.	Enrolment Number	Student Name	Thesis/Project Name
1.	19205001	KIYAM BABLOO SINGH	A STUDY OF PID CONTROLLER DESIGN USING CURRENT CONVEYOR BASED ACTIVE BLOCKS
2.	19205002	SANTOSH MUTUM	STUDY AND ANALYSIS OF 6 TRANSITOR STATIC RANDOM ACCESS MEMORY
3.	19205003	SAPAM BIKESH	GROWTH AND CHARACTERIZATION OF ALUMINIUM NANOPARTICLES ASSISTED SIO ₂ THIN FILM ON P-TYPE SILICON SUBSTRATE FOR PHOTODETECTOR APPLICATION
4.	19205004	DAIMOND SINGH SOIBAM	DESIGN AND IMPLEMENTATION OF THIRD ORDER LOW PASS DIGITAL FIR FILTER USING PIPELINING RETIMING TECHNIQUE
5.	19205005	RAJKUMAR MUMUSANA	DESIGN AND IMPLEMENTATION OF TUNABLE ALL PASS FILTER EMPLOYING CCDDCC
6.	19205006	TONGBRAM BIJESH SINGH	ECG QRS DETECTION USING MATLAB SIMULINK
7.	18205002	ROHIT SINGH PHEIROIJAM	STUDY AND DESIGN OF IOT BASED SMART IRRIGATION SYSTEM USING FPGA
8.		KETHA GANESH	DESIGN OF ROBUST RESPONSE TIME BASED PID FOR FREQUENCY CONTROL IN AN ISOLATED MICRO-GRID

2020 Batch			
Sl No.	Enrolment Number	Student Name	Thesis/Project Name
1.	18205001	ELANGBAM RAMESHWAR SINGH	FPGA IMPLEMENTATION OF DATA SCRAMBLER AND DESCRAMBLER SYSTEM USING UART FOR BLUETOOTH APPLICATION
2.	18205003	SALLY KHAIDEM	DETECTION AND REMOVAL OF IMPULSE NOISE FROM DIGITAL IMAGES BY FILTERS BASED ON FPGA
3.	18205004	RUBILA LAISHRAM	ELECTRONICALLY TUNABLE HIGH FREQUENCY THIRD ORDER OSCILLATOR EMPLOYING DXCCTA WITH MOS-C ELEMENTS
4.	18205005	KARAM BHARAT SINGH	DESIGN OF ELECTRONICALLY TUNABLE CURRENT MODE SECOND ORDER SIMO MOS-C MULTIFUNCTION FILTER USING DXCCTA FOR HIGH FREQUENCY OPERATION
5.	18205006	EDISON KHO	DESIGN AND IMPLEMENTATION OF FPGA BASED VENDING MACHINE FOR INTEGRATED CIRCUITS (ICs)
6.	18205007	CHANDAM THOISANA SINGH	BASIC APPLICATIONS USING FOUR TERMINAL FLOATING NULLOR TRANSCONDUCTANCE AMPLIFIER (FTFNTA)
7.	18205009	DAISY DEENRII ROEL	DESIGN AND IMPLEMENTATION OF HIGHER ORDER SIGMA DELTA MODULATORS CIRCUITS USING FPAA
8.	17205006	SUNIL KUMAR YADAV	DESIGN OF THIRD ORDER CURRENT TUNABLE OSCILLATOR USING SINGLE CCDDCC BLOCK



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2019 Batch			
Sl No.	Enrolment Number	Student Name	Thesis/Project Name
1.	17205012	NAOREM AKSHAYKUMAR	DESIGN AND IMPLEMENTATION OF MULTI-PROTOCOL CONVERTER/TRANSLATOR FOR SYSTEM ON CHIP
2.	17205011	KANGABAM VALERI	RECURRENT NEURAL NETWORK BASED MODEL FOR SOLVING THE ASSIGNMENT PROBLEM
3.	17205010	JACINTA POTSANGBAM	DESIGN AND IMPLEMENTATION OF COMBINED PIPELINING AND PARALLEL PROCESSING ARCHITECTURES FOR FIR AND IIR FILTER USING VHDL
4.	17205009	MILNER PAUL	EMBEDDED BASED SOLUTION FOR INTRACORTICAL AND INTRACRANIAL MICROSIMULATIONS FOR ASSESSING THE BEHAVIOUR OF RODENTS
5.	17205008	NAVIT KUMAR	ACTIVE BLOCK BASED INTEGER AND FRACTIONAL ORDER INDUCTORS AND ITS APPLICATION
6.	17205007	SHUVADEEP SEN	DESIGN OF APB PROTOCOL AND VERIFICATION USING UVM
7.	17205005	SUBHANJAN DAS	DESIGN OF WAVE ACTIVE FILTER USING ACTIVE BUILDING BLOCKS
8.	17205003	KARNI RAM	IMPLEMENTATION OF WORD LEVEL PARALLEL PROCESSING UNFOLDING ALGORITHM USING VHDL
9.	17205001	MARIM ALAN PHILLIMON	FPGA-BASED MONITORING SYSTEM FOR VARIOUS ENGINEERING APPLICATIONS
10.	16205010	SALAM NONGLENKHOMBA MEITEI	IMPLEMENTATION OF DIGITAL HYBRID TRANSFORM TECHNIQUES USING VERILOG FOR MULTIMEDIA APPLICATIONS.



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2018 Batch

SI No.	Enrolment Number	Student Name	Thesis/Project Name
1.	16205008	PAMU HARIKA	STUDY AND DESIGN OF SCHMITT TRIGGER AND ITS APPLICATION USING ANALOG ACTIVE BLOCKS
2.	16205005	ROHIT KUMAR SINGH	SYNTHESIS AND CHARACTERISATION OF TiO ₂ ZIGZAG NANOWIRE BY OBLIQUE ANGLE DEPOSITION TECHNIQUE FOR THE PHOTODETECTOR APPLICATION
3.	16205004	NITIN SINGHAL	GENERALIZE N-BIT BINARY COMPARATOR USING CYCLIC COMBINATIONAL METHOD
4.	16205003	ABHILASHA KUMARI	STUDY AND DESIGN OF SINUSOIDAL QUADRATURE OSCILLATOR USING EXTRA-X CURRENT CONTROLLED CURRENT CONVEYOR
5.	16205002	SUMIT KUMAR SINHA	STUDY AND DESIGN OF FULL-WAVE VOLTAGE RECTIFIER USING OTA
6.	16205001	PH NONGLEN MEITEI	STUDY AND DESIGN OF ACTIVE FILTERS AND ITS APPLICATION
7.	15MEC020	RAJKUMARI SANGITA DEVI	BARCODE MODULATION METHOD FOR DATA TRANSMISSION IN MOBILE DEVICES
8.	15MEC017	THINGOM CHARANJEET SINGH	THEFT DETECTION SYSTEM FOR SMART POWER GRID
9.	15MEC016	ANUPA HIJAM	DESIGN AND IMPLEMENTATION OF PULSE WIDTH MODULATION AND ITS APPLICATION
10.	15MEC019	YUMNAM SHANTIKUMAR SINGH	MINIMIZATION OF CHUA'S OSCILLATION CIRCUIT USING CFOA, DOCCII AND HYBRID REALIZATION
11.	15MEC018	RAJKUMARI LENIN	DEVELOPMENT OF PHONEME DATABASE FOR TEXT TO SPEECH CONVERSION FOR MANIPURI LANGUAGE USING SYLLABLE PHONEME APPROACH



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2017 Batch

SI No.	Enrolment Number	Student Name	Thesis/Project Name
1.	15MEC015	INDRA KUMARI	VERTICALLY ALIGN OF INDIUM OXIDE (In ₂ O ₃) NWs COATED WITH ALUMINIUM NANOPARTICLES
2.	15MEC014	PHEIROIJAM HOLYFIELD	DESIGN AND IMPLEMENTATION OF AMBA PROTOCOL USING VHDL
3.	15MEC013	MOHAMMAD ISMAEIL	FPGA BASED IMPLEMENTATION OF FILTERS FOR REMOVING IMPULSE NOISE
4.	15MEC012	VISTA J	STUDY AND DESIGN OF FRACTIONAL ORDER DEVICE AND ITS APPLICATIONS
5.	15MEC011	HEIGRUJAM MANAS SINGH	FPGA BASED REAL-TIME UNDERGROUND MINE ENVIRONMENT MONITORING AND WARNING SYSTEM
6.	15MEC010	PERUMALLA SUBRAHMANYAM	DESIGN OF ACTIVE FILTERS USING ADVANCE ANALOG BLOCKS
7.	15MEC009	DIVYA SINGH	ANALYSIS OF STRUCTURAL AND OPTICAL PROPERTIES OF ALUMINIUM NANOPARTICLES DECORATED ON SIOX NANOWIRES BY GLAD TECHNIQUE
8.	15MEC008	CHABUNGBAM AKENDRA SINGH	SIMULATION AND EXPERIMENTAL STUDY OF ZINC OXIDE THIN FILM DEPOSITED ON SILICON SUBSTRATE FOR OPTOELECTRONICS APPLICATIONS
9.	15MEC007	ABHISHEK KUMAR CHAUDHARY	DESIGN AND IMPLEMENTATION OF ADPLL FOR DIGITAL COMMUNICATION APPLICATIONS
10.	15MEC006	TERIRAMA THINGUJAM	ANALYSIS AND SIMULATION OF GRAPHENE BASED ELECTRONIC DEVICES
11.	15MEC005	RAVI KUMAR	MODELING OF BIOLOGICAL TISSUES BASED ON FRACTIONAL ORDER CIRCUIT
12.	15MEC004	L KHOLEE PHIMU	DESIGN AND IMPLEMENTATION OF AREA EFFICIENT 2-PARALLEL FIR DIGITAL FILTER
13.	15MEC003	BEAUTI KHATANIAR	DESIGN AND SIMULATION OF NOC ARCHITECTURE FOR UART USING ROUND ROBIN ARBITER
14.	15MEC002	TOURANGBAM HARISHORE SINGH	VLSI LOW POWER FIR FILTER
15.	15MEC001	SAMARTH GOYAL	FULLY PIPELINED AND HARDWARE EFFICIENT ARCHITECTURE TO IMPROVE PEAK SIGNAL TO NOISE RATIO FOR REAL-TIME DEMOSAICKING



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2016 Batch

SI No.	Enrolment Number	Student Name	Thesis/Project Name
1.	14MEC001	ARIBAM BALARAMPYARI DEVI	DESIGN AND IMPLEMENTATION OF DIFFERENT ADDER TOPOLOGIES
2.	14MEC002	ROSY KIMNEITHEM HAOKIP	OPTICAL AND STRUCTURAL ANALYSIS OF TiO ₂ NANOWIRES DEPOSITED ON GaN SUBSTRATE AND ITS PHOTODECTOR APPLICATIONS
3.	14MEC003	RAJKUMAR ANGAMBA SINGH	A MODIFIED SPEECH ENCHANCEMENT ALGORITHM BASED ON CONTINUOUS WAVELET TRANSFORM
4.	14MEC004	LONGJAM ABUNGCHA MEITEI	OPTICAL AND STRUCTURAL PROPERTIES ANALYSIS OF IN ₂ O ₃ THIN FILM DEPOSITED ON GaN SUBSTRATE BY PHYSICAL VAPOR DEPOSITION AND ITS OPTOELECTRONIC APPLICATIONS
5.	14MEC006	PHEIROIJAM POOJA	STRUCTURAL, OPTICAL AND ELECTRICAL PROPERTIES ANALYSIS OF VERTICALLY GROWN IN ₂ O ₃ NANOSTRUCTURES FOR THE PHOTODETECTOR APPLICATION
6.	14MEC007	PATRICK YUMNAM	SYNCHRONIZATION OF TRAFFIC LIGHTS IN AN ADAPTIVE TRAFFIC LIGHT CONTROL SYSTEM
7.	14MEC008	KHURAIJAM NELSON SINGH	IMPLEMENTATION OF HIGH SPEED MULTIPLIER IN FPGA
8.	14MEC010	SIDDARTH SHARMA	FPGA BASED IMPLEMENTATION OF MODIFIED VECTOR MEDIAN FILTER USING XILINX SYSTEM GENERATOR
9.	14MEC011	IMOPISHAK THINGOM	IMPLEMENTATION AND ANALYSIS OF MULTIPLIERLESS FIR FILTER ON FPGA
10.	14MEC012	SINAM AJITKUMAR SINGH	DESIGN AND IMPLEMENTATION OF 64 BITS PARALLEL CYCLIC REDUNDANCY CHECK GENERATOR FOR HIGH SPEED ETHERNET APPLICATION USING FPGA
11.	14MEC013	NGANGBAM MANOJKUMAR SINGH	HARDWARE BASED MEDICAL IMAGE FUSION USING DISCRETE WAVELET TRANSFORM
12.	14MEC014	MOHD. AZHARUDDIN	AN IMPROVED 64-BIT QCA ADDER